

Pin connections

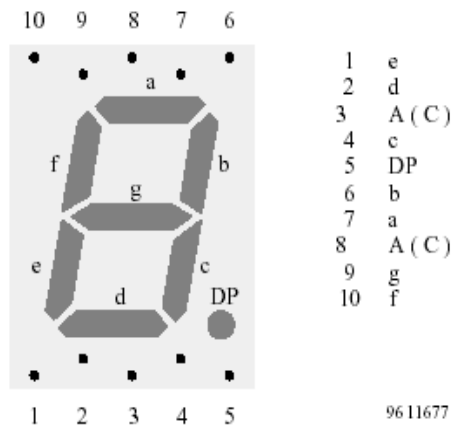


Figure 1

Table 1

MCU Port (Pin)	7 Segment Display
PTBD1 (12)	7 (a)
PTBD2 (14)	6 (b)
PTBD3 (16)	4 (c)
PTBD4 (18)	2 (d)
PTBD5 (20)	1 (e)
PTBD6 (22)	9 (f)
PTBD7 (24)	10 (g)

A : Common Anode – Connect to VCC(+)

C: Common Cathode – Connect to GND (-)

P_IO_5V	1	2	IRQ
GND	3	4	RESET_B
PTE0	5	6	BKGD
PTE1	7	8	PTG4
PTG0	9	10	PTB0
PTG1	11	12	PTB1
PTE2	13	14	PTB2
PTE3	15	16	PTB3
PTE6	17	18	PTB4
PTE5	19	20	PTB5
PTE7	21	22	PTB6
PTE4	23	24	PTB7
PTA0	25	26	PTC1
PTA1	27	28	PTC0
PTA2	29	30	PTG2
PTA3	31	32	PTG3
PTA4	33	34	PTF4
PTA5	35	36	PTF5
PTA6	37	38	PTC5
PTA7	39	40	PTC3

KABLONUN KIRMIZI TARAFI



I/O Connector (J35) Pinout